



Sheet 1 of 1

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY DOCKET NO. 1614.1116	APPLICATION NO. 09/768,630
LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)		FIRST NAMED INVENTOR Hideo MIYAKE et al.	
		FILING DATE January 25, 2001	GROUP ART UNIT 2183

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<i>dt</i>	AA	5,488,729	01/1996	Vegesna et al.	<b>RECEIVED</b> DEC 23 2003 Technology Center 2100		
<i>dt</i>	AB	6,012,139	01/2000	Biswas et al.			
<i>dt</i>	AC	6,018,798	01/2000	Witt et al.			
<i>dt</i>	AD	4,991,078	02/1991	Wilhelm et al.			
	AE						
	AF						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO	
<i>dt</i>	AG	0 793 168 A2	09/1997	European Patent Office			X	
<i>dt</i>	AH	0 503 968 A2	09/1992	European Patent Office			X	
<i>dt</i>	AI	0 394 162 A2	10/1990	European Patent Office			X	
<i>dt</i>	AJ	5-324274	12/1993	Japan			abs	
	AK							
	AL							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

				TRANSLATION YES NO	
<i>dt</i>	AM	Abraham, Seth, et al., "Instruction Reorganization for a Variable-Length Pipelined Microprocessor", PROCEEDINGS OF THE INTERNATIONAL CONFERENCE ON COMPUTER DESIGN: VLSI IN COMPUTERS AND PROCESSORS, New York, October 3-5, 1988, pages 96-101.			

EXAMINER <i>Amel Le</i>	DATE CONSIDERED 9/17/2004
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	